



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/834,660  
Filing Date ..... April 12, 2001  
Inventor..... Luan C. Tran  
Assignee..... Micron Technology, Inc.  
Group Art Unit..... 2813  
Examiner ..... L. Schillinger  
Attorney's Docket No. .... MI22-1637  
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor  
Processing Methods Of Forming Dynamic Random Access Memory Circuitry,  
And Related Integrated Circuitry

PRELIMINARY AMENDMENT

To: Mail Stop RCE  
Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

From: James D. Shaurette  
Tel. 509-624-4276; Fax 509-838-3424  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3817

EL979954655

Sir:

Please enter the following amendments prior to examining the above identified  
application:

AMENDMENTS